



#2
2663
04.6.5

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

ROBERT L. PAYNE, et al.

US 018186

Serial No. 09/997,609

Filed: Nov. 29, 2001

Group Art Unit: 2661

Title: High-Speed Interchip Interface Protocol

Commissioner for Patents
Washington, D.C. 20231

LETTER TO OFFICIAL DRAFTSMAN

RECEIVED
APR 03 2002
Technology Center 2600

Sir:

Enclosed are formal drawings for filing in the
above-identified application.

Respectfully submitted,

By
Michael Schmitt, Reg. 36,921
Attorney

Philips Electronics North
America Corporation
580 White Plains Road
Tarrytown, NY 10591
(408) 617-4745

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being
deposited this date with the United States Postal Service as
first-class mail in an envelope addressed to:

COMMISSIONER FOR PATENTS
Washington, D.C. 20231

On Mar. 21, 2002
(Mailing Date)

By Carol Bryant
(Signature)

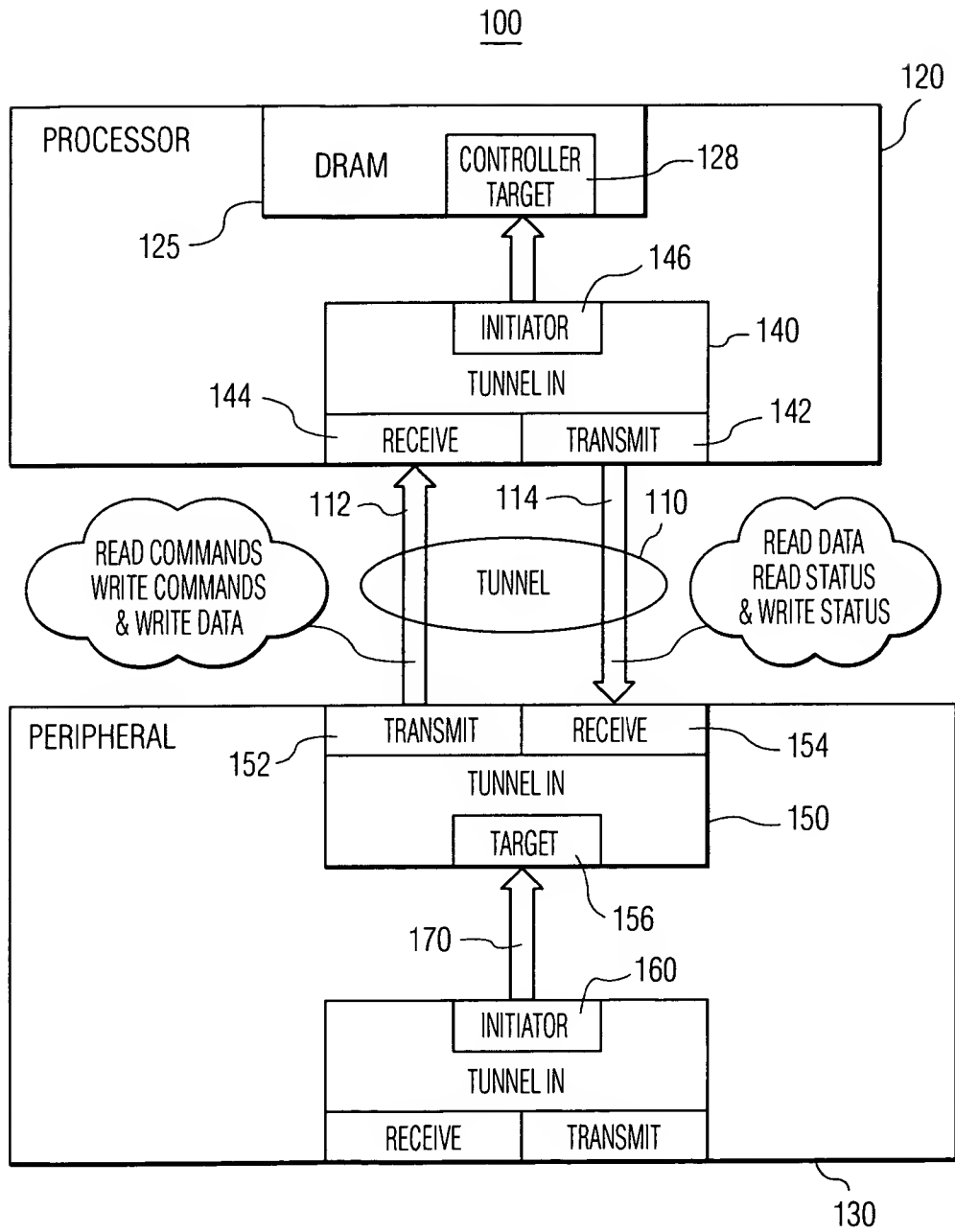


FIG. 1

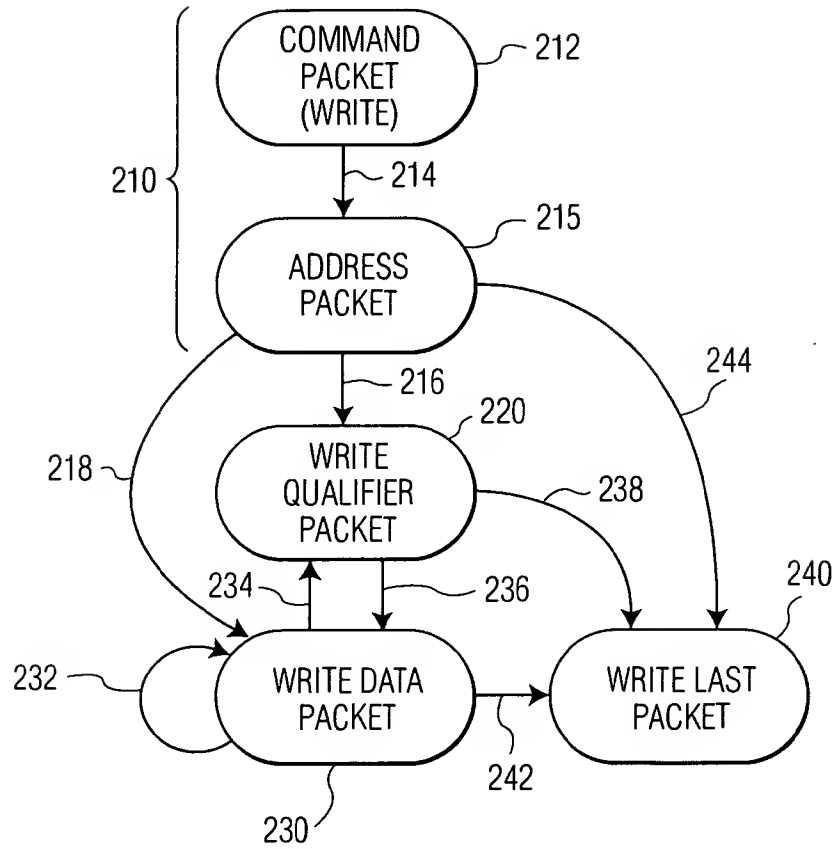


FIG. 2A

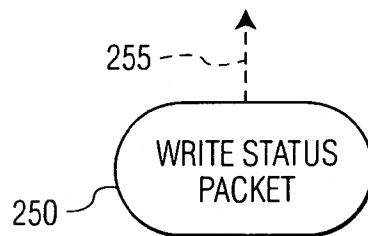


FIG. 2B

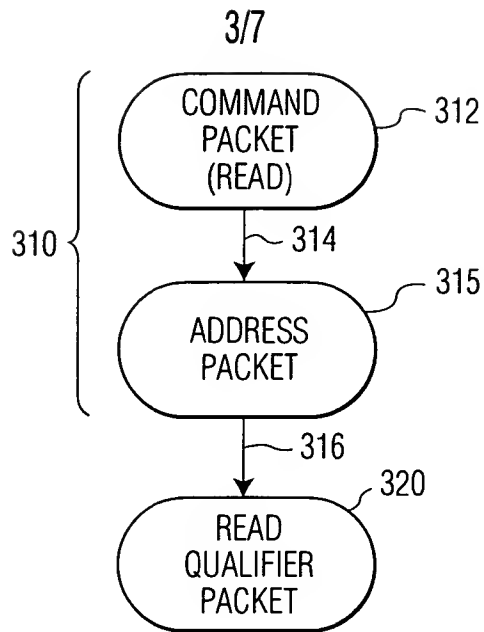


FIG. 3A

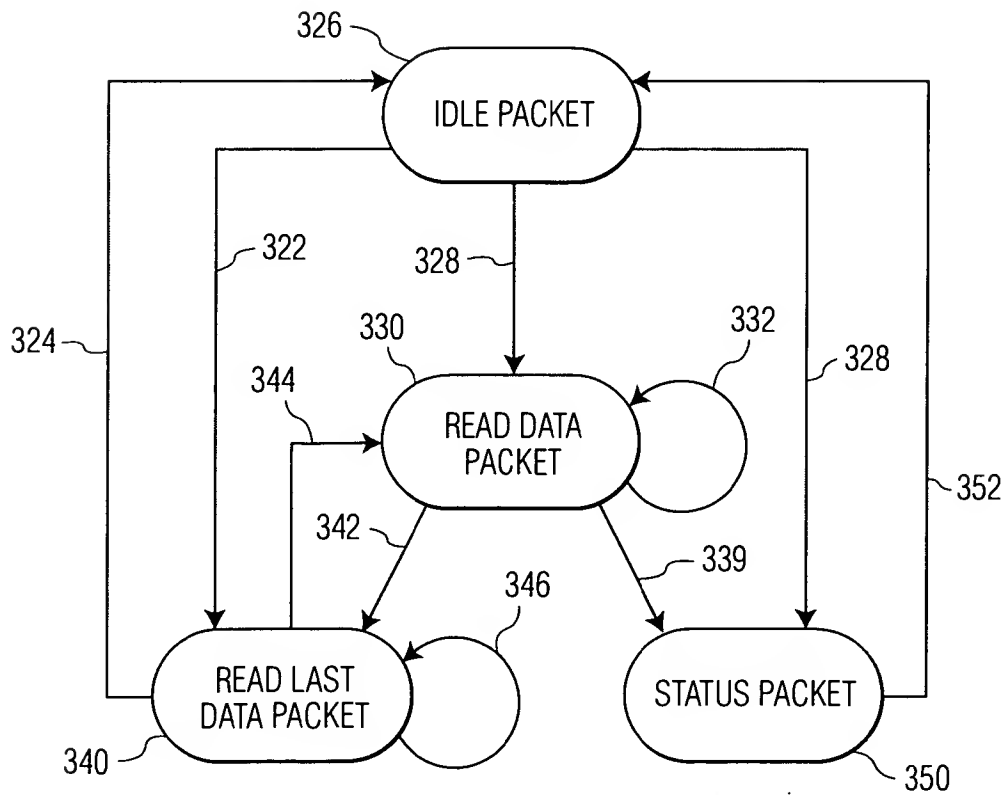


FIG. 3B

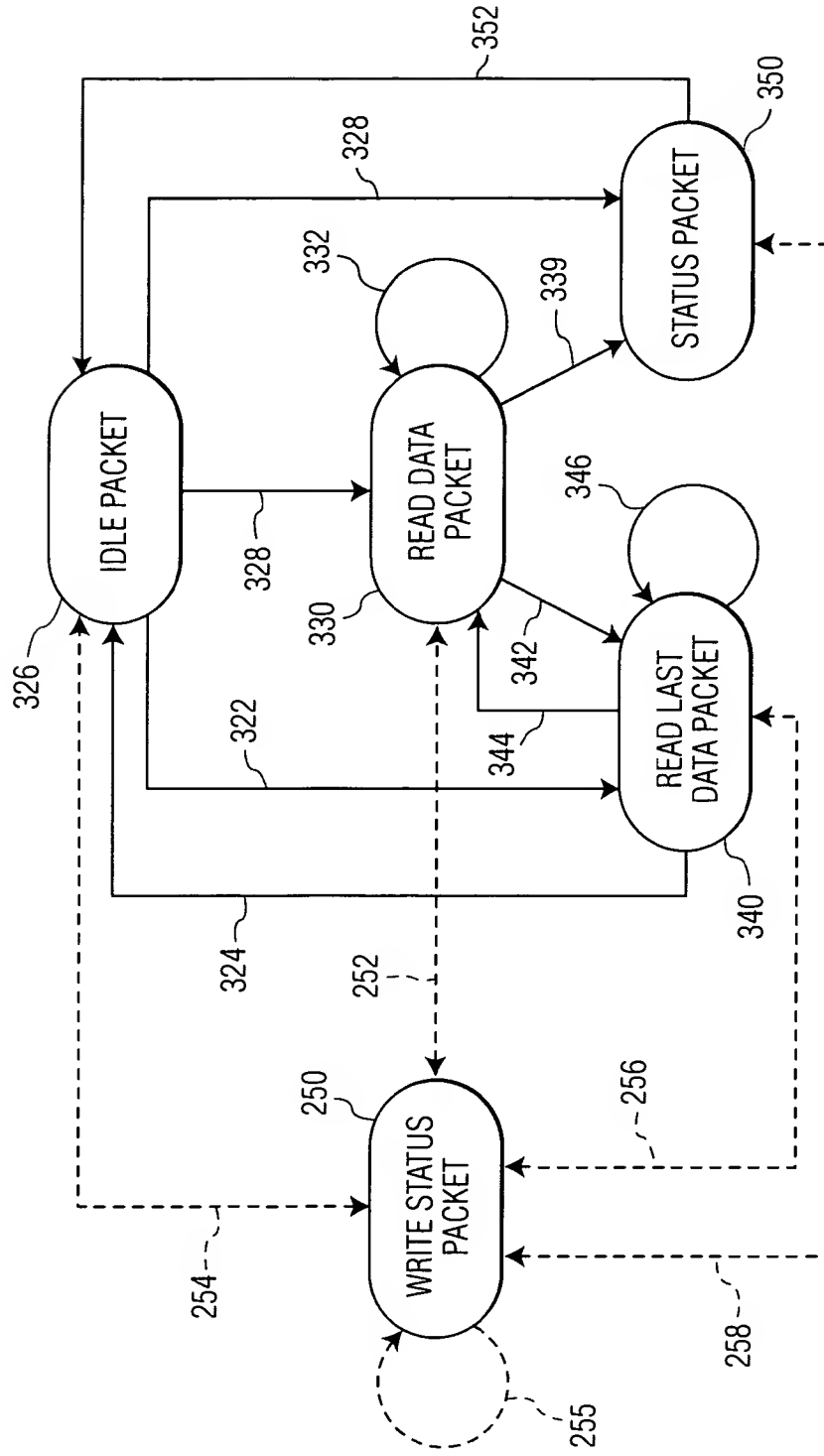


FIG. 4



FIG. 5

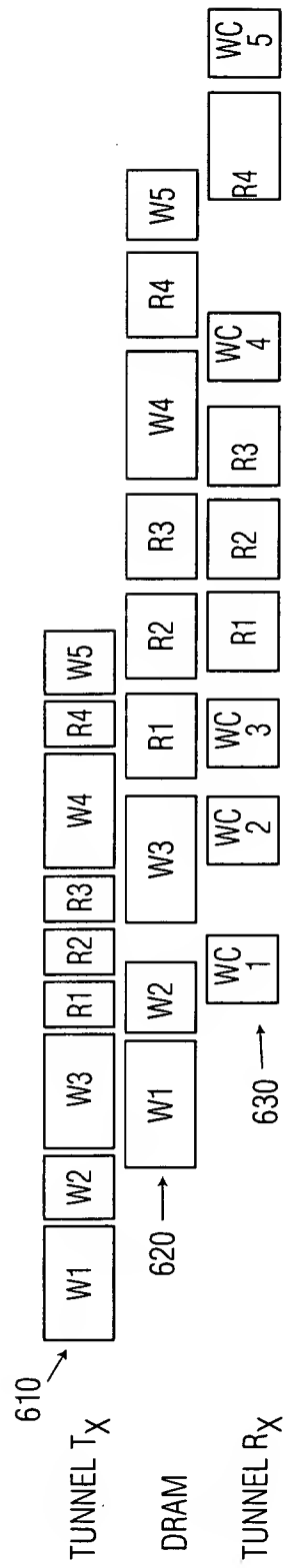


FIG. 6A

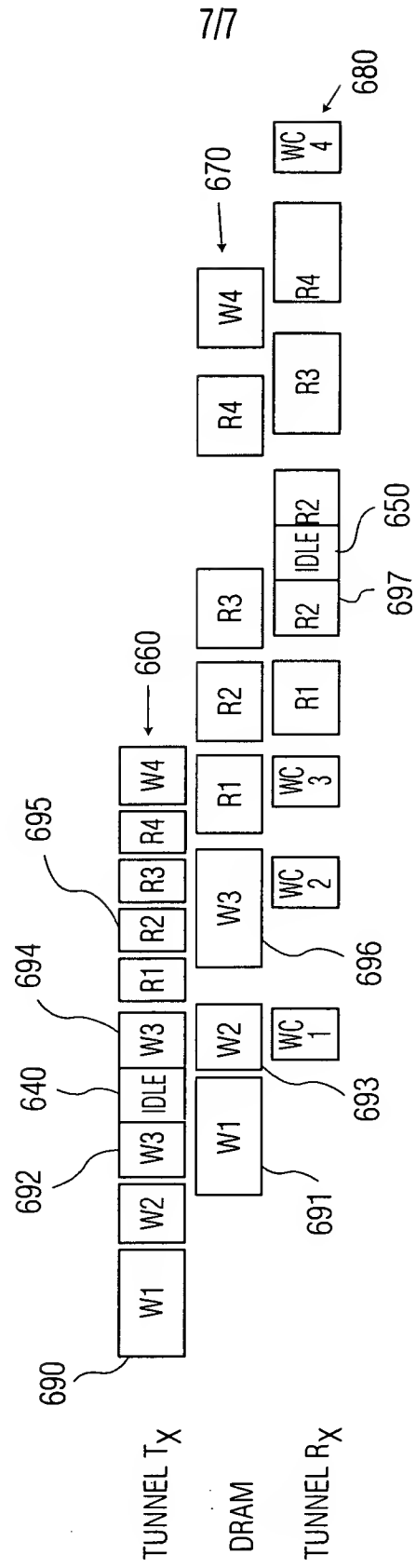


FIG. 6B